



PATENT LFW
Customer No. 22,852
Attorney Docket No. 04208.0204-00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| | | |
|-----------------------------|---|--------------------------|
| In re Application of: |) | |
| |) | |
| Takayuki WATANABE et al. |) | Group Art Unit: 2818 |
| |) | |
| Application No.: 10/501,349 |) | Examiner: Goodwin, D. J. |
| |) | |
| Filed: July 15, 2004 |) | |
| |) | |
| For: COMPOUND SEMICONDUCTOR |) | Confirmation No.: 7885 |
| MULTILAYER STRUCTURE, HALL |) | |
| DEVICE, AND HALL DEVICE |) | |
| MANUFACTURING METHOD |) | |

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

RESPONSE TO RESTRICTION REQUIREMENT

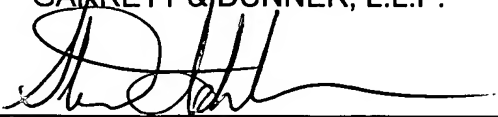
In a restriction requirement mailed April 27, 2006, the period for response to which extends through May 27, 2006, the Examiner required restriction under 35 U.S.C. § 121 between Group I (claims 1-17), characterized by the Examiner as being drawn to a semiconductor device; and Group II (claims 18-24), characterized by the Examiner as being drawn to a method of making a semiconductor device. Applicants elect to prosecute Group I (claims 1-17).

Please grant any extensions of time required to enter this response and charge any additional required fees to our deposit account 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

Dated: May 24, 2006

By: 
Steven L. Ashburn
Reg. No. 56,636